



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hiroshi HASHIMOTO**

Group Art Unit: **2814**

Serial Number: **10/083,533**

Examiner: **Thao X. Le**

Filed: **February 27, 2002**

PTO Confirmation No.: **6400**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A
SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD
CHARACTERISTIC**

Attorney Docket No.: **020244**

Customer No.: **38834**

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

April 22, 2004

Sir:

In response to the Office Actions dated December 19, 2003 and January 22, 2004, please amend the above-identified application as follows: